<u>CLAIMS</u>

Cancel claims 43-45.

46. (Original) A sliding correlator comprising:

a delay line having a plurality of delay elements for delaying a stream of data samples input thereto;

a plurality of remove chip units, each associated with an output of one of said plurality of delay elements, for removing chips from said delayed data samples;

an adder for receiving outputs from said plurality of remove chip units and adding said outputs together; and

an inhibit unit for selectively enabling at least one of said plurality of remove chip units and said adder.

47. (Original) The sliding correlator of claim 46, wherein said delay line is a circular buffer.

48. (Original) The sliding correlator of claim 46, wherein said inhibit unit operates to enable said at least one of said plurality of remove chip units and said adder only for delays of interest.

49. (Original) The sliding correlator of claim 46, wherein said inhibit unit selectively enables and disables said plurality of remove chip units and said adder together.